

FIG. 1

Digital Image Evolution Example

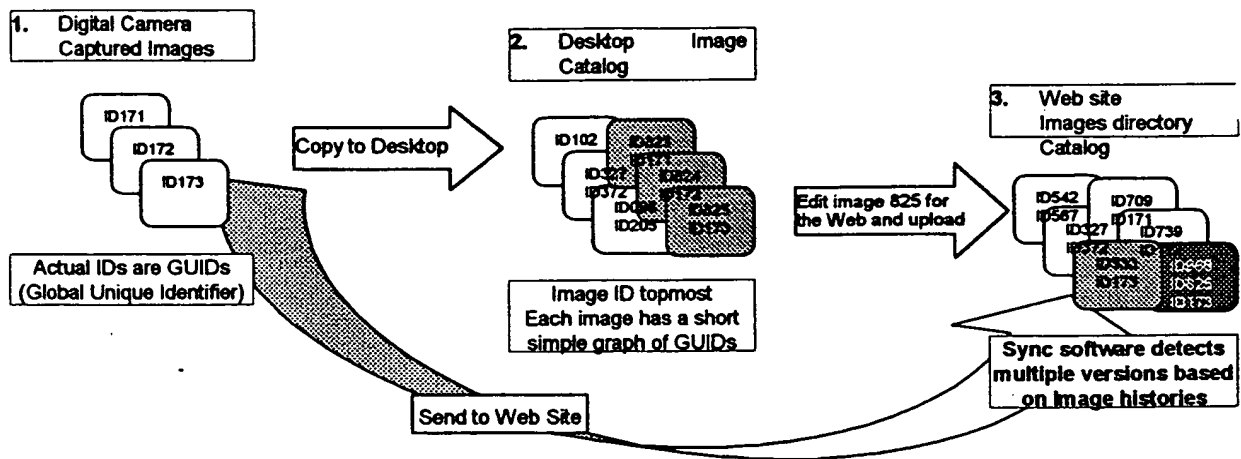


FIG. 2

FIG. 3 is a diagram illustrating a sequence of operations performed on a set of images, showing the progression from initial capture to final merging.

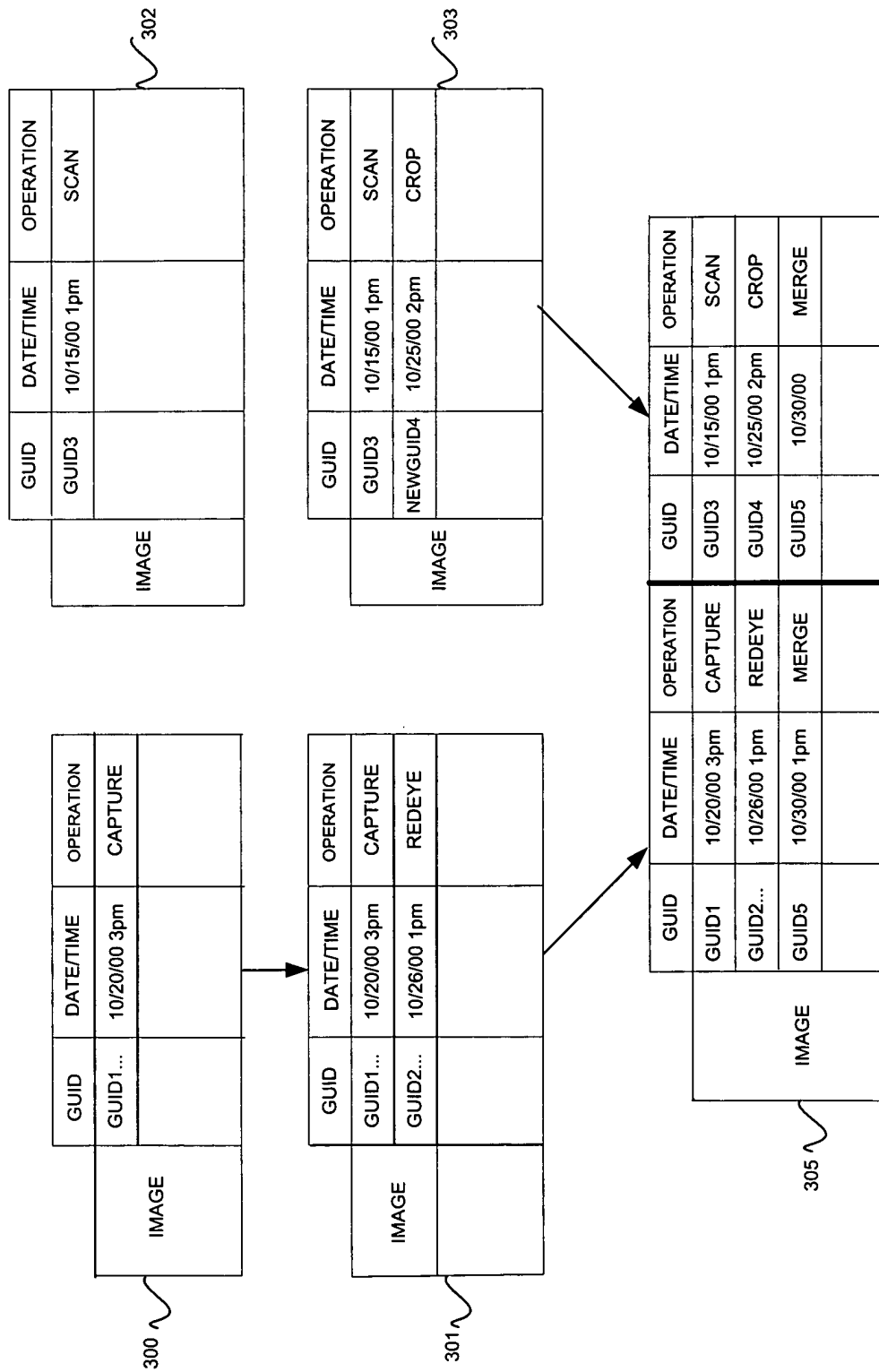


FIG. 3

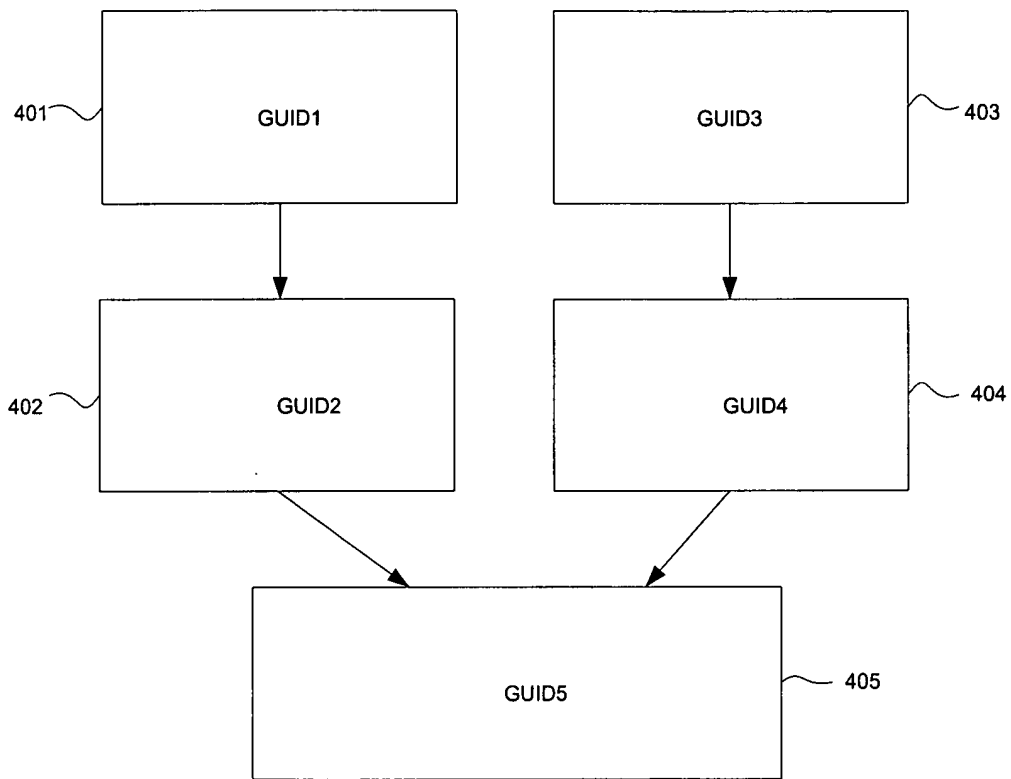


FIG. 4

FIG. 5A is a schematic diagram of a system 501. The system 501 includes a plurality of image processing units 57, 59, 67, and 68. The image processing units 57, 59, 67, and 68 are arranged in a vertical stack. The image processing unit 57 is at the top, followed by the image processing unit 59, then the image processing unit 67, and finally the image processing unit 68 at the bottom. The image processing units 57, 59, 67, and 68 are connected to a common bus 501. The system 501 is shown in a dashed box.

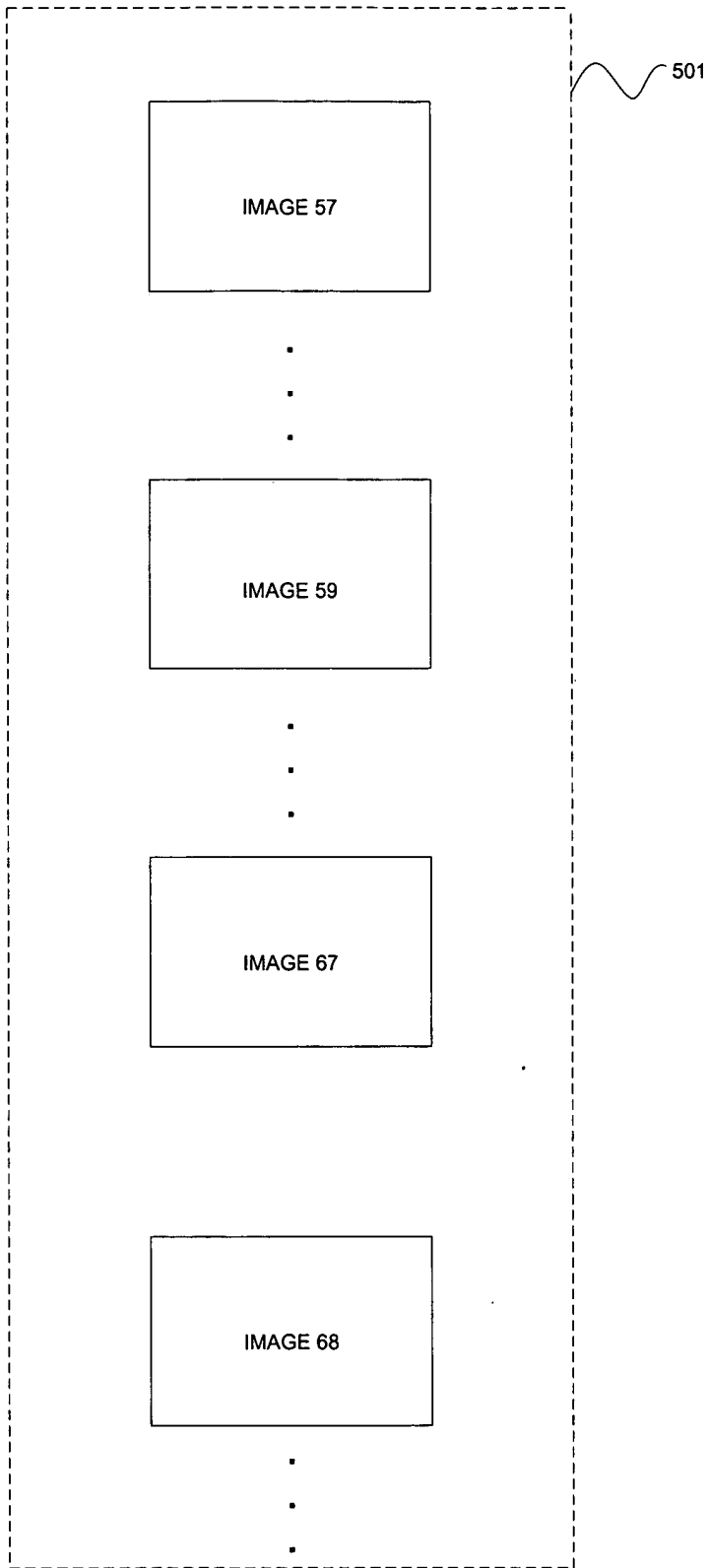


FIG. 5A

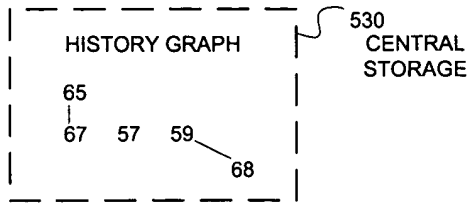


FIG. 5B

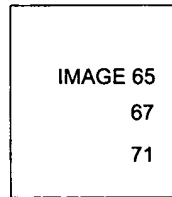


FIG. 5C

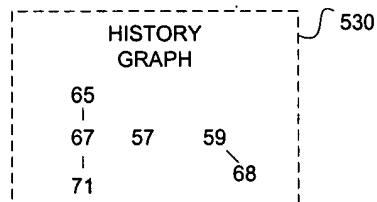


FIG. 5D

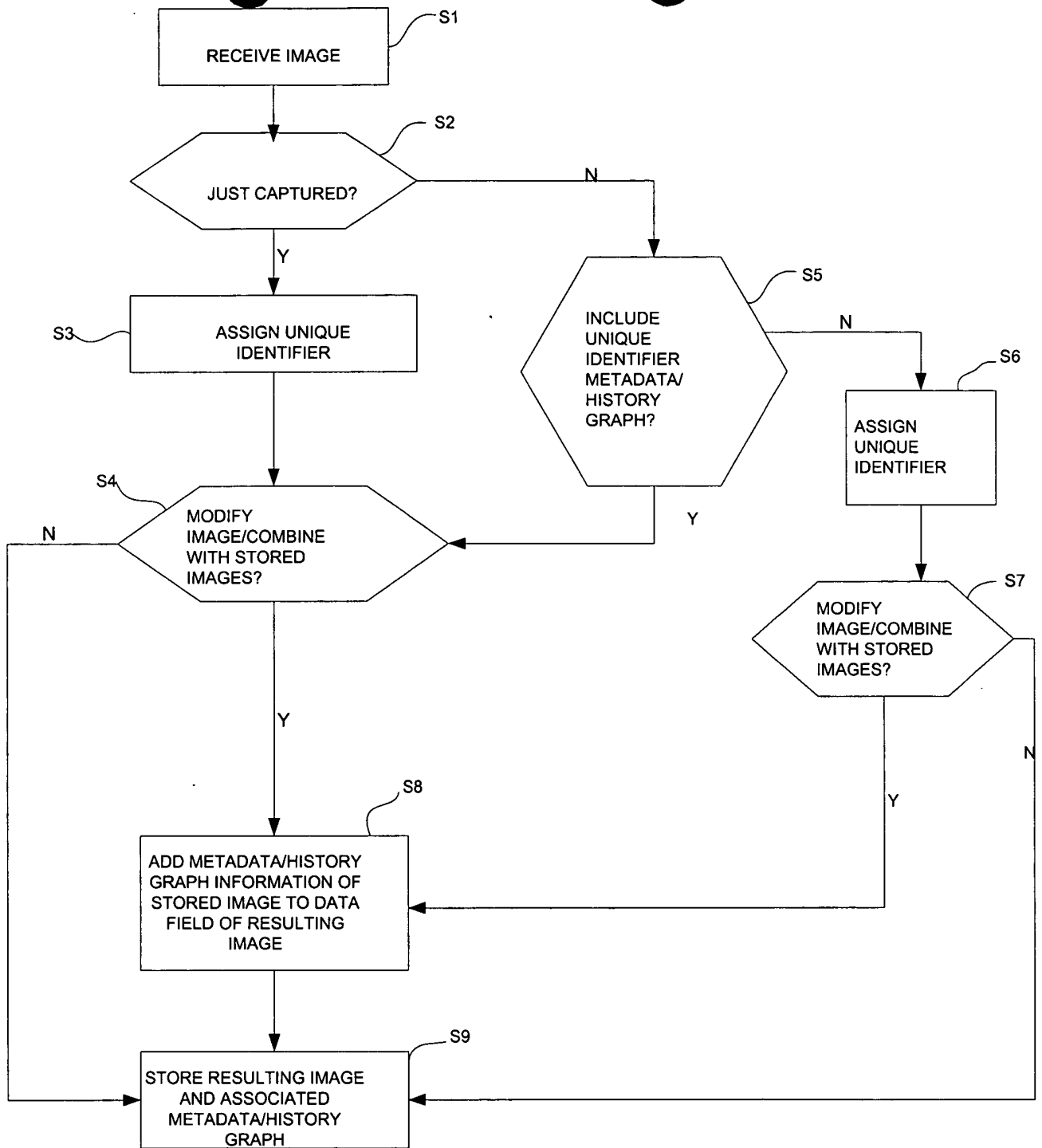


FIG. 6

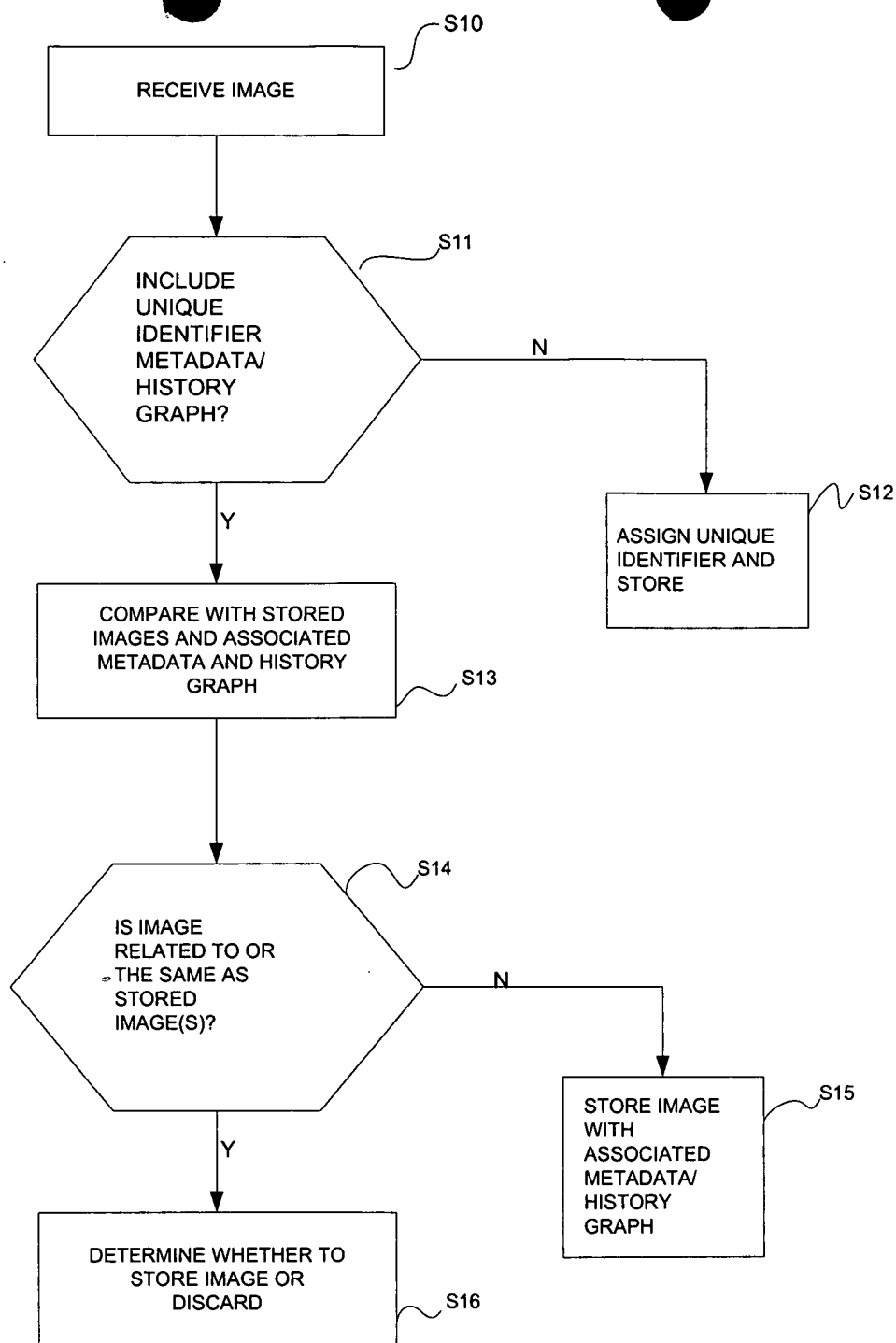


FIG. 7